

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)			<b>Complete If Known</b>		
			Application Number	Not Yet Assigned 10/790,880	
			Filing Date	March 3, 2004	
			First Named Inventor	Satoru AKIYAMA	
			Art Unit	2185	
			Examiner Name	TRM	
Sheet	1	of	1	Attorney Docket Number	500.43581X00

U.S. PATENT DOCUMENTS						
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
DT		US- 2003/0033492	A1	02/13/2003	Akiyama et al	
		US- 5,530,828		06/25/1996	Kaki et al	
		US- 5,999,474		12/07/1999	Leung et al	
		US- 6,075,740		06/13/2000	Leung	
		US- 6,285,826	B2	09/04/2001	Mizuno et al	
DT		US- 6,487,135	B2	11/26/2002	Watanabe et al	
		US-				
		US-				
		US-				
		US-				
		US-				
		US-				
		US-				
		US-				
		US-				

FOREIGN PATENT DOCUMENTS						
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				
DT		JP 7-093215		04/07/1995	Toshiba Corp.	AB

Examiner Signature	<i>Demayana</i>	Date Considered	10/24/06
-----------------------	-----------------	--------------------	----------

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 801.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard St.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449B/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(use as many sheets as necessary)</i>		<b>Complete if Known</b>	
		Application Number	Not Yet Assigned 10/790,880
		Filing Date	March 3, 2004
		First Named Inventor	S. AKIYAMA et al
		Group Art Unit	2185
		Examiner Name	TRM
Sheet 2 of 2	Attorney Docket Number	500.43581X00	

OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published	T <sup>2</sup>
DT		ATWOOD, Bryan et al, "SESO Memory: A CMOS compatible with high density embedded memory technology for mobile application," 2002 Symposium on VLSI Circuit Digest of Technical Papers, pp. 152-155, (2000) USA. 12 pages	
DT		LAI, Stefan et al, "OUM - A 180 nm nonvolatile memory cell element technology for stand alone and embedded applications," 2001 IEEE International Electron Devices Meeting Digest of Technical Papers, pp. 803-806 (2001), USA. 4 pages	
DT		"Combination Memories" searched on the Internet on April 28, 2003 <URL:http://www.sharp-world.com/products/device/flash/cmllst.html> page 1-3,	
DT		HWANG, Chong-Lii et al, "A 2.9ns Random Access Cycle Embedded DRAM with a Destructive-Read Architecture", 2002 Symposium on VLSI Circuits Digest of Technical Papers, pp. 174-175, USA.	

Examiner Signature	Deusepam	Date Considered	11/10/06
--------------------	----------	-----------------	----------

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.